PATENT ABSTRACTS OF JAPAN

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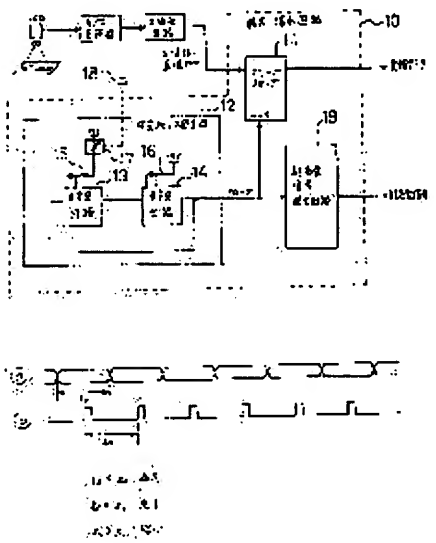
(54) ENLARGING/REDUCING SYSTEM FOR INPUT IMAGE

(57)Abstract:

PURPOSE: To improve the image input performance of an image scanner by changing an enlarging ratio and a reducing ratio optionally and continuously.

CONSTITUTION: A flip-flop 11 has a function to sample binarization image data from a CCD with a clock CLK. At the output of the flip-flop 11, reduced and expanded variable power data appear by the length of the period of the clock CLK. The period of binary data supplied from the CCD to the flip-flop 11 is t0, the period of the clock CLK supplied from a variable clock generating device 12 to the flip-flop 11 is tS, and then, an expanding image is obtained at the time of setting to tS<t0, a full size is obtained at the time of tS=t0 and at the time of setting to tS>t0, the reducing image is obtained. A clock period tS is changed by a knob 18 of a variable resistance 17 in the variable clock generating device 12.



LEGAL STATUS

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